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M122
 === Diagnostics ===
RepRapFirmware for Duet 2 WiFi/Ethernet version 3.1.1 running on Duet Ethernet 1.02 or later
Board ID: 0JD0M-9P6B2-NJ4S4-6JTD8-3SD6J-TV1QL
Used output buffers: 1 of 24 (2 max)
 === RTOS ===
Static ram: 27980
Dynamic ram: 92448 of which 140 recycled
Exception stack ram used: 256
Never used ram: 10248
Tasks: NETWORK(ready,1200) HEAT(blocked,1276) MAIN(running,1824) IDLE(ready,80)
Owned mutexes:
 === Platform ===
Last reset 00:00:28 ago, cause: power up
Last software reset time unknown, reason: User, spinning module GCodes, available RAM 10032 bytes (slot 1)
Software reset code 0x0003 HFSR 0x00000000 CFSR 0x00000000 ICSR 0x0441f000 BFAR 0xe000ed38 SP 0xffffffff Task MAIN
Error status: 0
MCU temperature: min 19.1, current 21.7, max 22.0
Supply voltage: min 1.5, current 1.7, max 1.7, under voltage events: θ, over voltage events: θ, power good: no Driver θ: ok, SG min/max not available
Driver 1: ok, SG min/max not available
Driver 2: ok, SG min/max not available
Driver 3: ok, SG min/max not available
Driver 4: ok, SG min/max not available
Date/time: 1970-01-01 00:00:00
Cache data hit count 54857974
Slowest loop: 4.16ms; fastest: 0.09ms
I2C nak errors 0, send timeouts 0, receive timeouts 0, finishTimeouts 0, resets 0
 === Storage ===
Free file entries: 10
SD card 0 detected, interface speed: 20.0MBytes/sec
SD card longest read time 3.1ms, write time 0.0ms, max retries 0
Hiccups: Θ(θ), FreeDm: 169, MinFreeDm: 169, MaxWait: Θms
Bed compensation in use: none, comp offset 0.000
 === MainDDARing ===
Scheduled moves: θ, completed moves: θ, StepErrors: θ, LaErrors: θ, Underruns: θ, θ CDDA state: -1
 === AuxDDARing ===
Scheduled moves: θ, completed moves: θ, StepErrors: θ, LaErrors: θ, Underruns: θ, θ CDDA state: -1
=== Heat ===
priver i: ok, so min/max not avaitable
Driver 2: ok, SG min/max not available
Driver 3: ok, SG min/max not available
Driver 4: ok, SG min/max not available
Date/time: 1970-01-01 00:00:00
Cache data hit count 54857974
Slowest loop: 4.16ms; fastest: 0.09ms
I2C nak errors \theta, send timeouts \theta, receive timeouts \theta, finishTimeouts \theta, resets \theta
=== Storage ===
Free file entries: 10
SD card \theta detected, interface speed: 20.0MBytes/sec
SD card longest read time 3.1ms, write time \theta.0ms, max retries \theta
=== Move ==
Hiccups: Θ(Θ), FreeDm: 169, MinFreeDm: 169, MaxWait: Θms
Bed compensation in use: none, comp offset 0.000
=== MainDDARing ===
Scheduled moves: θ, completed moves: θ, StepErrors: θ, LaErrors: θ, Underruns: θ, θ CDDA state: -1
=== AuxDDARing ==
Scheduled moves: 0, completed moves: 0, StepErrors: 0, LaErrors: 0, Underruns: 0, 0 CDDA state: -1
=== Heat ===
Bed heaters = 0 -1 -1 -1, chamberHeaters = -1 -1 -1 -1
=== GCodes ===
Segments left: 0
Movement lock held by null
HTTP is idle in state(s) 0
Telnet is idle in state(s) 0
File is idle in state(s) 0
USB is ready with "M122" in state(s) 0
Aux is idle in state(s) 0
Trigger is idle in state(s) 0
Queue is idle in state(s) 0
Daemon is idle in state(s) 0
Autopause is idle in state(s) 0
Code queue is empty.
 === Network ===
--- Network --- Slowest loop: 0.39ms; fastest: 0.01ms
Responder states: HTTP(θ) HTTP(θ) HTTP(θ) HTTP(θ) FTP(θ) Telnet(θ), θ sessions
HTTP sessions: θ of 8
Interface state active, link 100Mbps full duplex
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